## Optimus (XRG90) >> Faultfind

## - Power ON Results

## - sw/Hw - versions Release 2.3

This screen displays the actual firmware versions **Release**, **V**ersion (don't care) and **Level**. For the PCB **C**entral **U**nit it also displays the hardware version (must be **1** for Release 2.x).

```
[R2V1L3]
CU APPL SW:
                           4512 114 20823
                           4512 113 20721
CU BOOT SW:
                [R2V1L3]
FU kV:
                           4512 113 20121
                [R2V1L1]
FU mA a:
                           4512 113 20205
                [R0V1L5]
FU mA b:
FU mA c:
                [----]
FU mA d:
                [----]
FU CIE:
                [R0V1L1]
                           4512 113 20301
                                              as standard version
FU HI a:
                [R2V1L3]
                           4512 113 20523
FU HI b:
                [----]
FU RC a:
                [R1V1L2]
                           4512 113 22312
FU RC b:
FU RC c:
                [----]
FU AD a(1WA):
                [R0V1L1]
                           4512 113 20601
FU AD b(2WA):
                [R0V1L1]
                           4512 113 20601
FU AD c(1WB):
                [----]
FU AD d(2WB):
                [----]
FU MDO:
                [----]
CU HW:
                    [ 1]
```

## - sw/Hw - Versions Release 3.3

This screen displays the actual firmware versions **Release**, **V**ersion (don't care) and **Level**. For the PCB **C**entral **U**nit it also displays the hardware version (must be **2** for Release 3.x).

```
CU APPL SW:
                [R3V1L3]
                           4512 114 20833
CU BOOT SW:
                [R3V1L1]
                           4512 113 20731
FU kV:
                           4512 113 20132
                                              as an example for Optimus RAD
                [R3V1L2]
FU mA a:
                [R1V1L2]
                           4512 113 20212
                [-----1
FU mA b:
                [----]
FU mA c:
                [-----]
FU mA d:
FU CIE:
                [R1V1L1]
                                              as standard version
                           4512 113 20311
FU HI a:
                [R2V1L4]
                           4512 113 20524
FU HI b:
                [----]
FU RC a:
                [R2V1L2]
                           4512 113 22322
FU RC b:
                [----]
                [----]
FU RC c:
FU AD a(1WA):
                [R1V1L1]
                           4512 113 20611
FU AD b(2WA):
                [R1V1L1]
                           4512 113 20611
                           4512 113 20611
FU AD c(1WB):
                [R1V1L1]
FU AD d(2WB):
                [----]
FU MDO:
                [----]
CU HW:
                    [ 2]
```